Introduction to Programming in Python

Building a Computer: Logic Circuits

Outline

1 Boolean Functions

2 Logic Circuits

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The three basic boolean functions: $not(x) = \bar{x}$, or(x, y) = x + y, and $and(x, y) = x \cdot y$

The truth table for a boolean function is a listing of all possible input values, together with the output value

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Truth tables for not, or, and and functions

x	x
0	1
1	0

x	у	x + y
0	0	0
0	1	1
1	0	1
1	1	1

x	у	x · y
0	0	0
0	1	0
1	0	0
1	1	1

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The algorithm

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 - b. Combine all of the variables using \cdot
- 4. Combine all of the minterms using +

Example: consider the proposition "if you score over 93% in this course, then you will get an A"

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x	У	$x \implies y$	minterm
0	0	1	
0	1	1	
1	0	0	
1	1	1	

Example: consider the proposition "if you score over 93% in this course, then you will get an A"

x	у	$\mathbf{x} \implies \mathbf{y}$	minterm
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Example: consider the proposition "if you score over 93% in this course, then you will get an A"

x	у	$x \implies y$	minterm
0	0	1	$ar{x}\cdotar{y}$
0	1	1	
		0	
1	1	1	

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Example: consider the proposition "if you score over 93% in this course, then you will get an A"

The proposition is described by the implication function $(x \implies y)$

x	У	$x \implies y$	minterm
0	0	1	$ar{x}\cdotar{y}$
0	1	1	$ar{x} \cdot y$
1	1	1	$x \cdot y$

Therefore, $implication(x, y) = \bar{x} \cdot \bar{y} + \bar{x} \cdot y + x \cdot y$

The circuits (called gates) that implement the not, or, and and functions



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The circuit for the implication function $\bar{x} \cdot \bar{y} + \bar{x} \cdot y + x \cdot y$



A full adder (FA) circuit can add two 1-bit numbers (with carry) to produce a 2-bit result



х	у	c _{in}	z	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

An n-bit ripple-carry adder for adding two n-bit numbers is n FA circuits chained together

An *n*-bit ripple-carry adder for adding two *n*-bit numbers is n FA circuits chained together

Example (2-bit ripple-carry adder for adding two 2-bit numbers)



Truth table for a nor gate (or followed by not)

x	у	$\overline{\mathbf{x}+\mathbf{y}}$
0	0	1
0	1	0
1	0	0
1	1	0

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x	у	$\overline{\mathbf{x}+\mathbf{y}}$
0	0	1
0	1	0
1	0	0
1	1	0

A 1-bit memory circuit, called a latch, built using two nor gates



S	R	Q	Q
0	0	0	1
1	0	1	0
0	0	1	0
0	1	0	1
0	0	0	1

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0	0	1
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Billion latches can be combined to produce a 1GB Random Access Memory (RAM) module